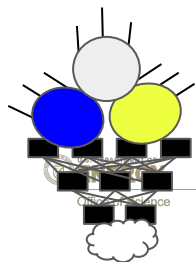




GELATO Update 3/29/24

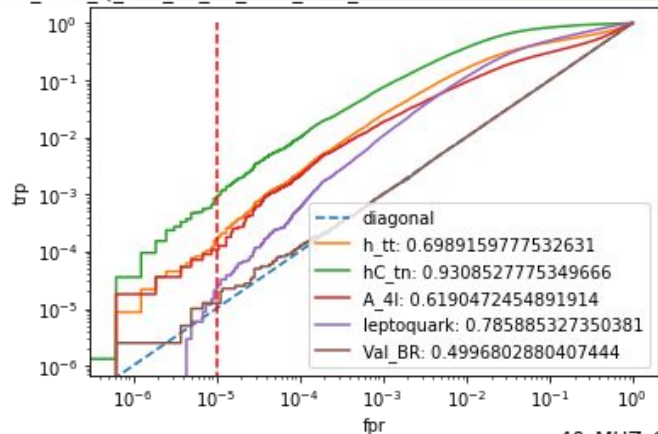
Liam Sherman



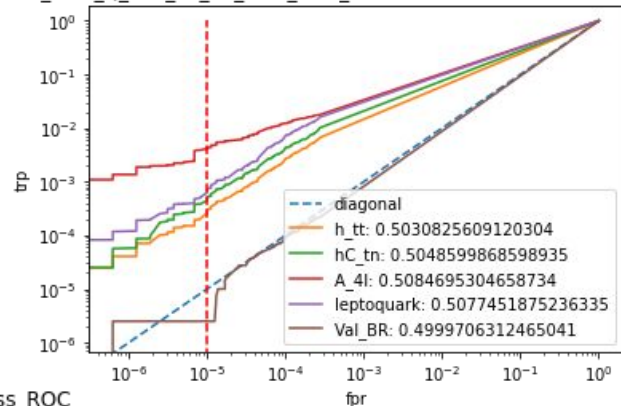
New 16 bit ROC's

SLAC

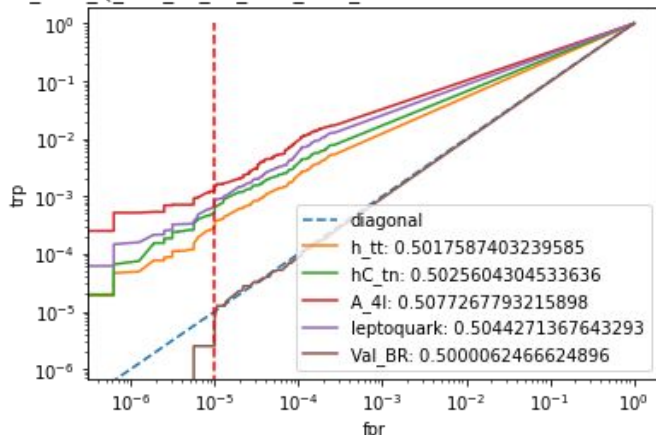
40_MHZ_Q_VAE_16_bit_beta_0.83_batchnorm KL Trained MSE Loss_ROC



40_MHZ_Q_VAE_16_bit_beta_0.83_batchnorm KL Trained KL Loss_ROC

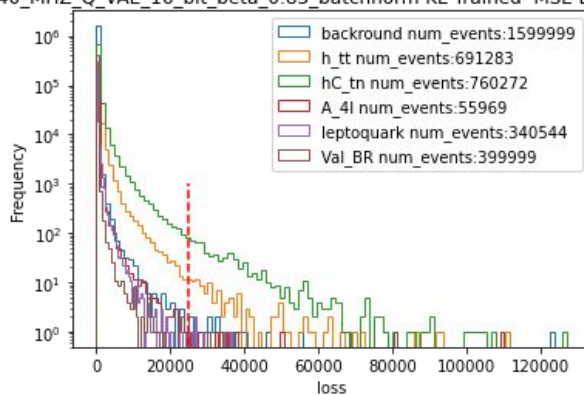


40_MHZ_Q_VAE_16_bit_beta_0.83_batchnorm KL Trained CKL Loss_ROC

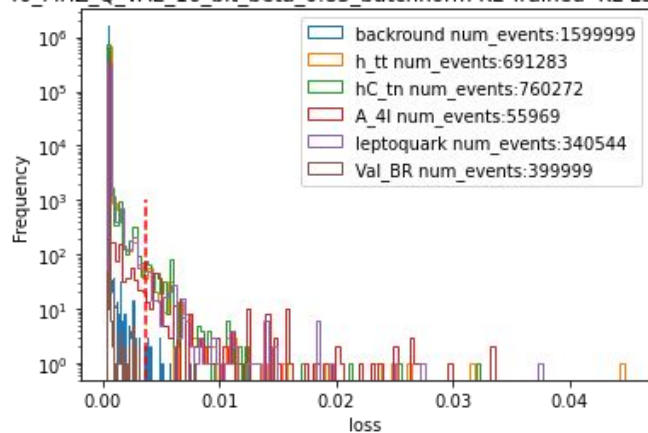


16 bit Histograms

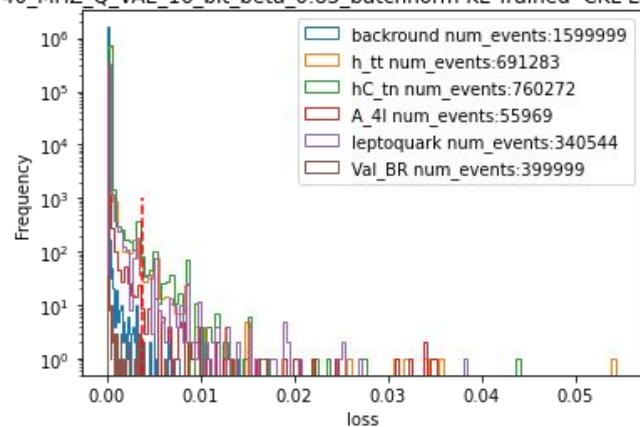
40_MHZ_Q_VAE_16_bit_beta_0.83_batchnorm KL Trained MSE Loss_Hist



40_MHZ_Q_VAE_16_bit_beta_0.83_batchnorm KL Trained KL Loss_Hist

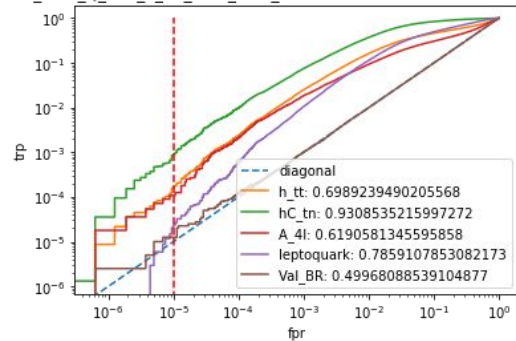


40_MHZ_Q_VAE_16_bit_beta_0.83_batchnorm KL Trained CKL Loss_Hist

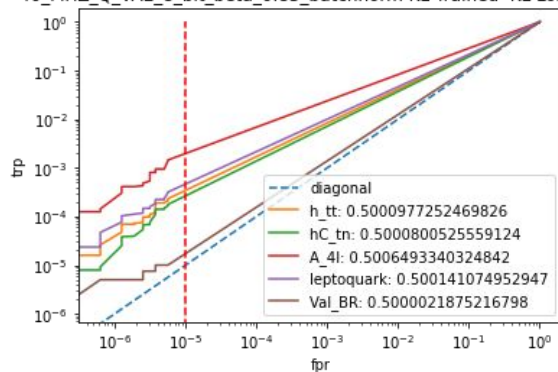


8 bit ROC

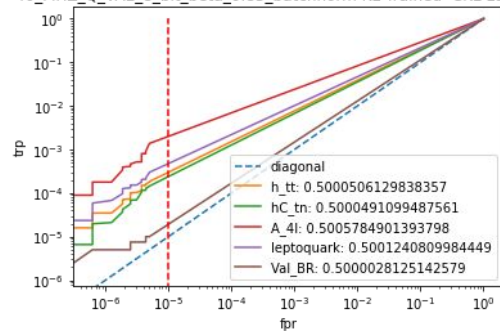
40_MHZ_Q_VAE_8_bit_beta_0.83_batchnorm KL Trained MSE Loss_ROC



40_MHZ_Q_VAE_8_bit_beta_0.83_batchnorm KL Trained KL Loss_ROC



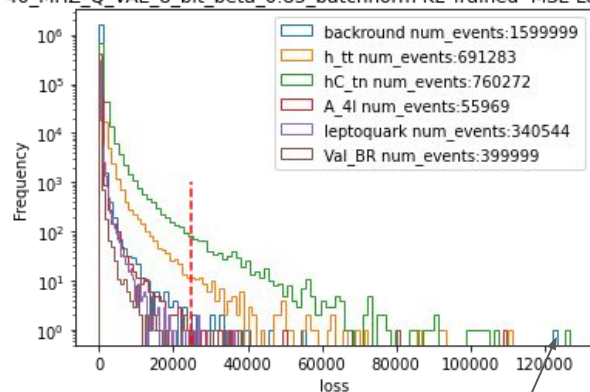
40_MHZ_Q_VAE_8_bit_beta_0.83_batchnorm KL Trained CKL Loss_ROC



8 bit histogram

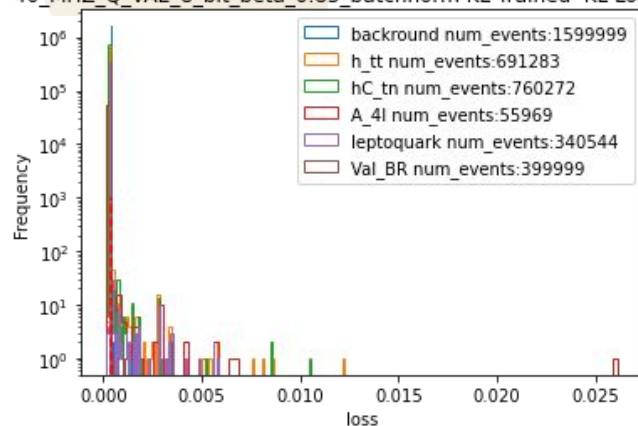
SLAC

40_MHZ_Q_VAE_8_bit_beta_0.83_batchnorm_KL_Trained_MSE_Loss_Hist



Data event at high loss

40_MHZ_Q_VAE_8_bit_beta_0.83_batchnorm_KL_Trained_KL_Loss_Hist



40_MHZ_Q_VAE_8_bit_beta_0.83_batchnorm_KL_Trained_CKL_Loss_Hist

