

## Session Program

Nov 7 - 10, 2023



## CPAD Workshop 2023

### ***RDC5***

SLAC, 51/1-102 - Kavli Auditorium  
2575 Sand Hill Rd, Menlo Park, CA 94025, USA

# Tue, November 7

4:00 PM

## RDC5: Session #1 (Trigger and DAQ)

**Session** | **Location:** SLAC, 51/3-305 - Kavli 3rd Floor | **Conveners:** Jinlong Zhang, Zeynep Demiragli

4:00 – 4:20 PM **Introduction to RDC5**

**Speakers**

Jinlong Zhang, Zeynep Demiragli

4:20 – 4:40 PM

**Empowering AI Implementation: The Versatile SLAC Neural Network Library (SNL) for FPGA, eFPGA , ASIC**

**Speaker**

Abhilasha Dave

4:40 – 5:00 PM

**Machine learning based developments for LHC level-1 triggers**

**Speaker**

Sridhara Dasu

5:00 – 5:20 PM

**Future detector readout**

**Speaker**

Alexander Paramonov

5:20 – 5:40 PM

**LuSEE Night Electronics Design**

**Speaker**

Ivan Kotov

6:00 PM

# Thu, November 9

4:00 PM

## RDC5: Session #2

**Session** | **Location:** SLAC, 51/3-305 - Kavli 3rd Floor | **Conveners:** Zeynep Demiragli, Jinlong Zhang

4:00 – 4:20 PM **HLS In A DAQ Environment**

**Speaker**

J.J. Russell

4:20 – 4:35 PM

**hls4ml: deploying deep learning on FPGAs for L1 trigger and Data Acquisition**

**Speaker**

Mia Liu

4:35 – 4:50 PM

**An Open Source General Purpose DMA Engine For DAQ Systems**

**Speaker**

Ryan Herbst

4:50 – 5:05 PM

**Portable Acceleration of CMS Mini-AOD Production with Coprocessors as a Service**

**Speaker**

Mia Liu

5:05 – 5:20 PM

**Yet Another Rapid Readout - For ATLAS Inner Tracker during HL-LHC**

**Speaker**

Angira Rastogi

5:20 – 5:30 PM

**An In-Network Event Builder for the Mu2e TDAQ System**

**Speaker**

Sean Cummings

5:30 – 5:45 PM

**Rogue: Back-End Integration and Future Developments**

**Speaker**

Omar Moreno

5:45 – 6:00 PM

**Discussion**

6:00 PM