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GAMPix

Electron Track Detection with a Dual Scale LArTPC System

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GAMPix detector

Electron Track Detection with a Dual Scale System in TPCs

Low - Noise Threshold Power

Diffusion Independent

(Can use diffusion to estimate drift length)

GAMPix detector

Electron Track Detection with a Dual Scale System in TPCs

Low - Noise Threshold Power







Possible Application in DUNE FD Phase 2





Low-energy "blips" produced by neutrons and electrons in DUNE events.

- Many of those are below threshold in current readout schemes

Proposed GAMPix detector enables the detection of low energy scatters

- Greatly improves the Supernova neutrino (MeV scale) detection.

A. Friedland and S. W. Li, "Understanding the energy resolution of liquid argon neutrino detectors," Phys. Rev. D 99, no.3, 036009 (2019) [arXiv:1811.06159 [hep-ph]]

How to Image the Electron Tracks?

1000 keV, 21.2K e-

- **Charge loss** when diffusion is on the scale of sensors

Challenges:



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1000 keV, 21.2K e-

Example of **Pixels Only** Readout



How to Image the Electron Tracks?

Challenges:

- **Charge loss** when diffusion is on the scale of sensors

- Low noise needs high power

Noise sets the

- energy resolution

- threshold





- <u>Pixel chips:</u>
- Image track



Coarse (cm-pitch) induction grids followed by fine grained pixels

- <u>Coarse grids:</u>
 - Measure charge **integral**, with $\sigma_e \simeq 15 e^{-1}$
 - Provide trigger signal (address) to pixel chips
- <u>Pixel chips:</u>
 - Image track
 - Power-up in < 1 µsec following coarse trigger
 - Power, reduced by $10^{3-}10^{4}$, to ~1 W/m²



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GAMPix | Simulated Signal Chain



GAMPix | Simulated Signal Chain Issue: Non-uniform response from the wire grid



GAMPix | Simulation Results

<u>GammaTPC</u>

Submillimeter (0.25mm) Resolution Pixelated Track Imaging

Energy Resolution of <1%

for electron tracks >100 keV ($2k e^{-}$) with an ENC of $20e^{-}$ on pixels and $30e^{-}$ on wires.

Low Power Consumption ~ 1 W/m²

Due to the **triggering** capability and **fast pixel power cycling**, the pixel plane has a low duty-cycle. Energy saving by a factor of 10^3 to 10^4

Drift Length Estimation via Diffusion with ~5% accuracy



Outcomes

Electronic noise reduction from ~500e- to ~50e- lowers threshold for "blip" detection from >1MeV to **much less**

Power cycling the pixels' front-end reduces the average power to acceptable levels

Drift Length Estimation via Diffusion with ~5% accuracy (Helps with background rejection for SN)

GAMPix fast power cycle ASIC



- Builds on pixelated ASICs from SLAC TID-ID, cryogenic SoC ASIC development for nEXO/DUNE and quantum instrumentation
- 130nm CMOS process (foundry + SLAC custom cryo models)
- Preliminary transistor-level modeling of power switching CSA: ΔT_{on} ~500 ns, $\sigma_e \sim 25e-$



I would like to continue talking about...

- Other possible applications of GAMPix
- GAMPix hardware test
- GammaTPC

- Machine learning for GAMPix
 - + Depth estimation via diffusion
 - + Initial electron direction estimation



Upcoming test with CRYO ($\sigma_e \sim 100e$ -) readout

BACKUP Slides

Track reconstruction promising



Initial direction



- Powerful measurement of initial direction
- Also: ~5% estimate of drift distance

GAMPix for DUNE



Geometry:

- ~ 5mm pitch pixels
- ~ 5 cm pitch of unified grid

Outcomes

A pixel-based readout system that reduces that electronic noise from ~500e- to ~50elowers threshold for "blip" detection from >1MeV to much less

- An "activation" signal from a coarse electrode "grid" to power the pixels' frontend and reduce the **average power to acceptable levels**

 Drift length estimation via diffusion measure (with ~ 5% accuracy)